

REMARKS

Claims 1-3 are all the claims pending in the application. Claims 1-3 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Jones *et al.*, U.S. Patent 6,094,187, and the disclosure on pages 1-2 of the specification.

I. Formal Matters

The Examiner has not acknowledged receipt of Applicants' formal drawings filed on November 7, 2001. Applicants respectfully requests that Examiner acknowledge receipt of the formal drawings in the next Office Action.

The Examiner has not acknowledge Applicants' claim for foreign priority. Applicants respectfully request that the Examiner acknowledge Applicants' claim for foreign priority in the next Office Action.

II. 35 U.S.C. § 103(a) Rejection

The Examiner has rejected claims 1-3 under 35 U.S.C. § 103(a) as being unpatentable over Jones in view of the disclosure on pages 1-2 of the specification. Applicants respectfully traverse the Examiner's rejections.

Claim 1:

In Jones, the pixel of Fig. 5a is comprised of a plurality of sub-pixels 56, 57. See col. 8, lines 7-11; Fig. 5a. Each sub-pixel 56, 57 is driven by a separate data signal D_{1a}, D_{1b} (col. 8, lines 11-15) provided to the sub-pixel 56, 57 by the data signal generator 30 (col. 7, lines 15-18).

Unlike Jones, in the Applicants' claimed invention a *single* input image signal is provided to each discharge cell which may be comprised of sub-fields. The single input image signal comprises respective pixel data for driving the sub-fields. In other words, each sub-fields is driven by the same input image signal.

Accordingly, Jones provides does not teach or suggest a method of driving a plasma display panel wherein a single input image signal drives a plurality of sub-fields. Therefore, Applicants therefore respectfully request that the Examiner withdrawal the rejection.

Claim 2:

Claim 2 is dependent on claim 1. Applicants submit that claim 2 is patentable at least based on its dependency on claim 1. Therefore, Applicants respectfully request that the Examiner withdraw the rejection.

Claim 3:

Claim 3 recites claim language similar to claim 1. For the reasons set forth above with respect to claim 1, Applicants submit that claim 3 is patentable over the cited art. Therefore, Applicants respectfully request that the Examiner withdraw the present objection.

III. Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

Amendment Under 37 C.F.R. § 1.111
U.S. Appln. No.: 09/883,448

Attorney Docket # Q64974

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

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WASHINGTON OFFICE

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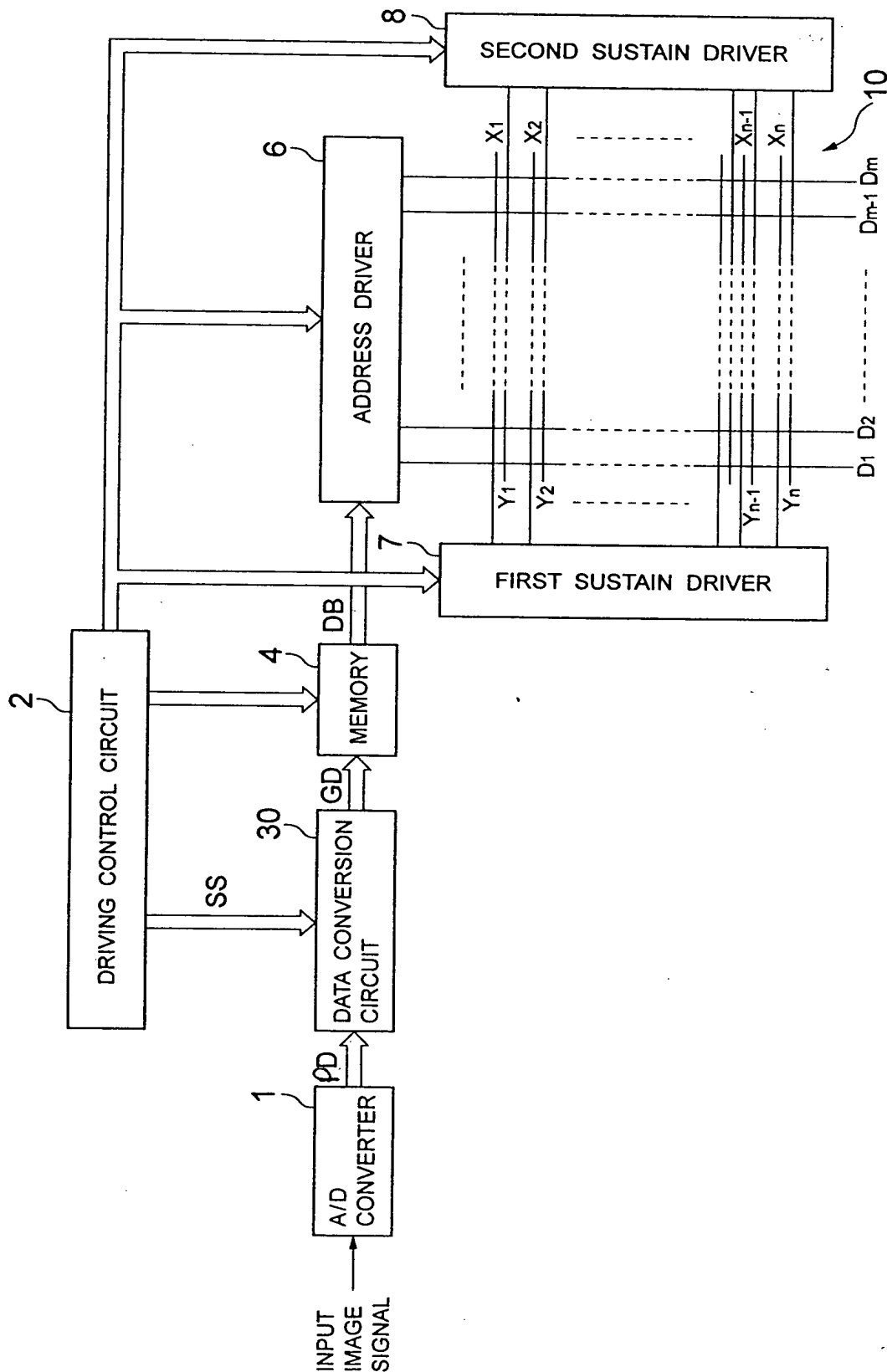
CUSTOMER NUMBER

Pat. Filed Aug. 33, 102
Grant K. Rowan
Registration No. 41,278

Date: September 24, 2003



FIG. 1





Appln. No. 09/883,448

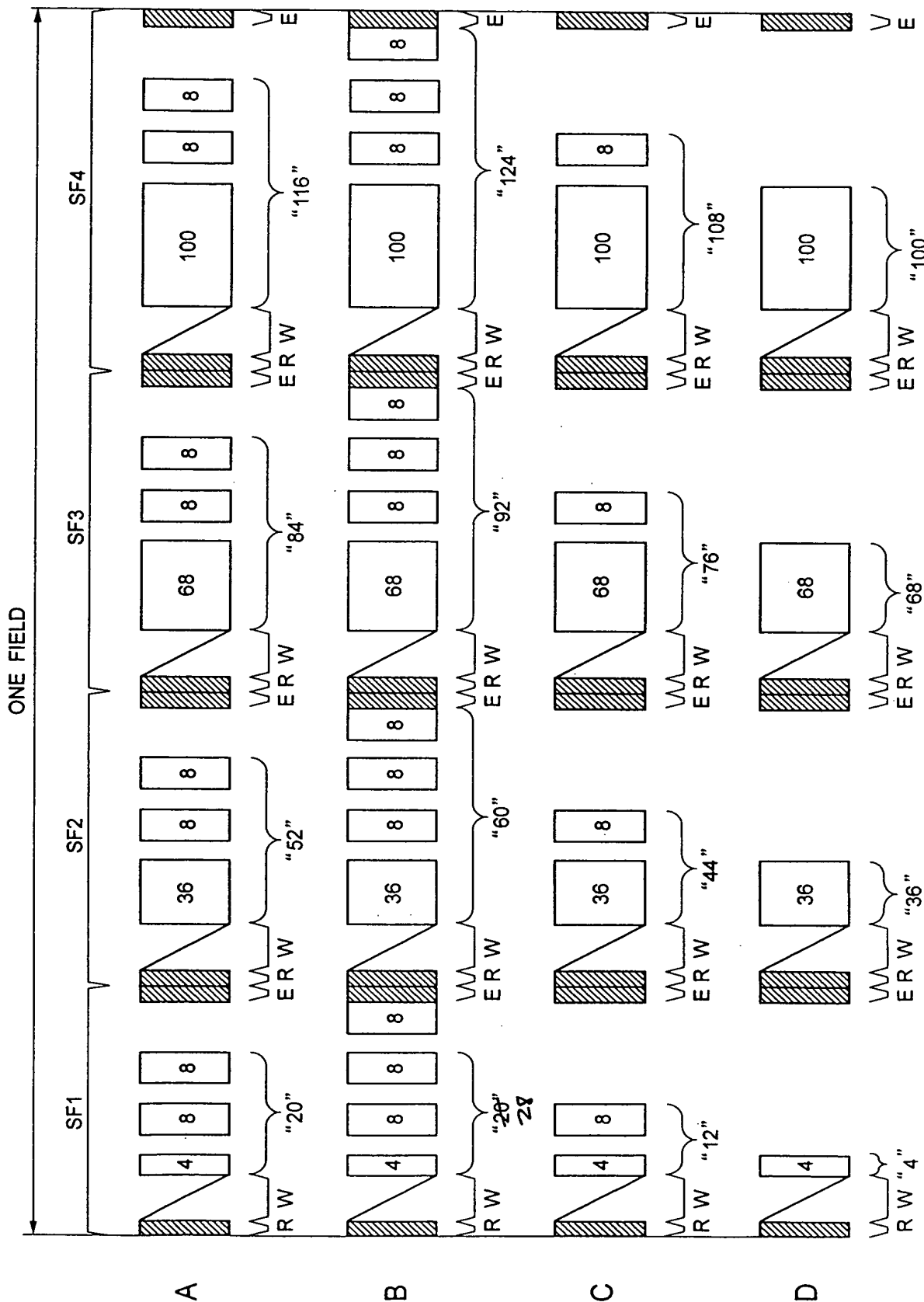
Docket No. Q64974

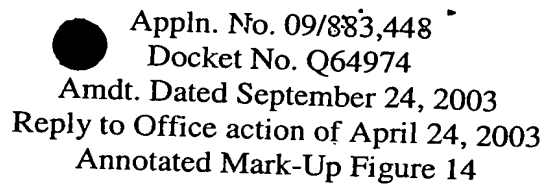
Amdt. Dated September 24, 2003

Reply to Office action of April 24, 2003

Annotated Mark-Up Figure 8

FIG. 8





The diagram illustrates the timing for one sub-field, divided into a WRITE phase and an ERASE phase. The horizontal axis represents time, with labels for column electrodes (D1, D2, D3, D4, ..., DPn) and row electrodes (X1, X2, X3, X4, ..., Xn). The vertical axis represents the row electrodes (Y1, Y2, Y3, Y4, ..., Yn). The diagram shows the relationship between the column electrodes, row electrodes, and the row electrodes (Y1, Y2, Y3, Y4, ..., Yn) during the WRITE and ERASE phases. The WRITE phase is indicated by a bracket labeled "WRITE" and the ERASE phase by a bracket labeled "ERASE". The diagram shows the relationship between the column electrodes, row electrodes, and the row electrodes (Y1, Y2, Y3, Y4, ..., Yn) during the WRITE and ERASE phases. The WRITE phase is indicated by a bracket labeled "WRITE" and the ERASE phase by a bracket labeled "ERASE".